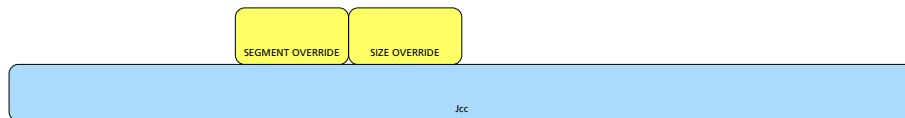




x86 Opcode Structure and Instruction Overview

0	1	2	3	4	5	6	7	8	
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XCHG EAX

MMX, SSE{1,2,3}, VMX

Jcc SHORT

ROL/ROR/RCL/RCR/SHL/SHR/SAL/SAR

CONDITIONAL LOOP

EXCLUSIVE ACCESS

CONDITIONAL REPETITION

